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Bouchard 3-7-4-4-7-2

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application

Applicant(s): G.A. Bouchard et al.
Case: 3-7-4-4-7-2
Serial No.: 10/029,679
Filing Date: December 21, 2001
Group: 2152
Examiner: To Be Assigned

I hereby certify that this paper is being deposited on this date with the U.S. Postal Service as first class mail addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Signature: Laura M. Hamlin Date: May 20, 2003

Title: Methods and Apparatus for Using Multiple Reassembly
Memories for Performing Multiple Functions

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Technology Center 2100

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicants' attorney wishes to bring to the attention of the Patent and Trademark Office the following documents listed on the accompanying Form PTO-1449. Copies of the listed documents are enclosed together with the search report that listed these documents.

Foreign Patent Documents

1. PCT International Application WO 01/16682 A1 published 03/08/01
2. PCT International Application WO 99/21326 published 04/29/99

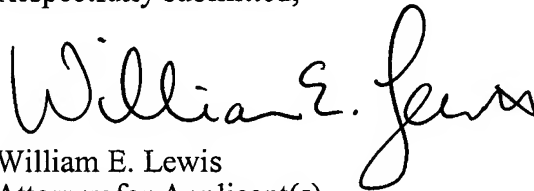
Other Documents

1. E.P. Rathgeb et al., "Redundancy Concepts for a Large ATM Switching Node," XVI World Telecom Congress Proceedings, pp. 425-433, 1997.
2. G. Parulkar et al., "AITPM: A Strategy for Integrating IP with ATM," Computer Communication Review, No. 4, pp. 49-58, 1995.

The undersigned certifies in accordance with 37 C.F.R. §1.97(e)(1) that each item listed above was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement. It is believed that there is no fee due in conjunction with the filing of this Information Disclosure Statement. In the event of non-payment or improper payment of a required fee, the Commissioner is authorized to charge or to credit **Ryan, Mason & Lewis, LLP Deposit Account No. 50-0762** as required to correct the error.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability, or as a representation that no other material information exists.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "William E. Lewis". The signature is fluid and cursive, with a large initial "W" and a stylized "L".

Date: May 20, 2003

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FORM PTO-1449 (MODIFIED)

**LIST OF PUBLICATIONS FOR
APPLICANT'S INFORMATION
DISCLOSURE STATEMENT**



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U.S. PATENT DOCUMENTS

EXAMINER					FILING DATE
INITIAL	DOCUMENT NO.	DATE	NAME	CLASS/SUBCLASS	IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

EXAMINER					TRANSLATION
INITIAL	DOCUMENT NO.	DATE	COUNTRY	CLASS/SUBCLASS	YES NO
1.	WO 01/16682 A1	03/08/01	PCT		
2.	WO 99/21326	04/29/99	PCT		

OTHER DOCUMENTS

EXAMINER				
INITIAL	REF NO.	AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.		
1.	E.P. Rathgeb et al.,	"Redundancy Concepts for a Large ATM Switching Node," XVI World Telecom Congress Proceedings, pp. 425-433, 1997.		
2.	G. Parulkar et al.,	"AITPM: A Strategy for Integrating IP with ATM," Computer Communication Review, No. 4, pp. 49-58, 1995.		

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Examiner

Date Considered

Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.